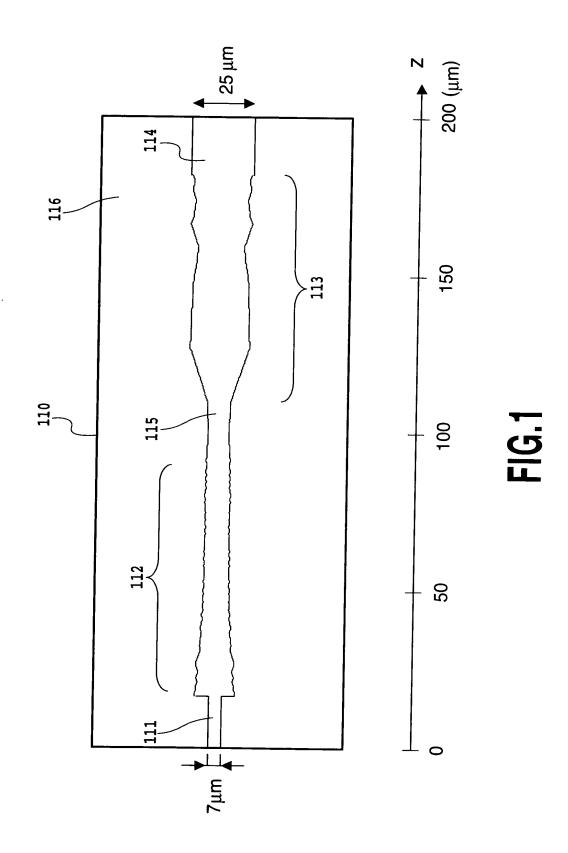
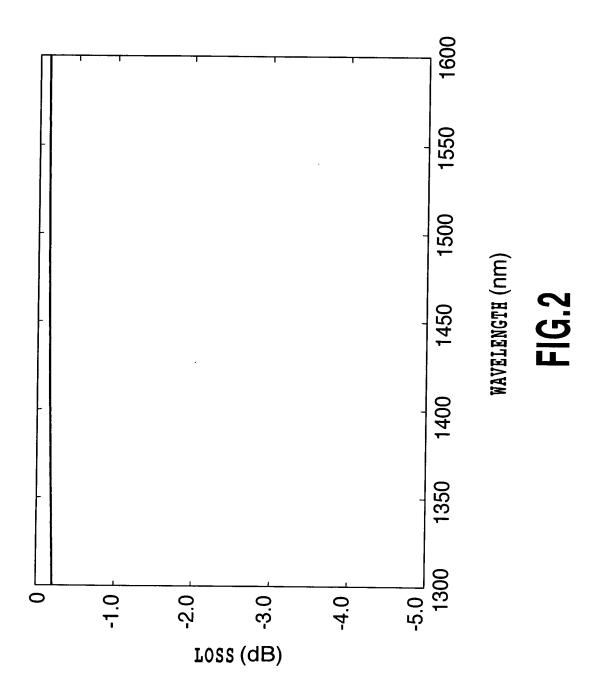
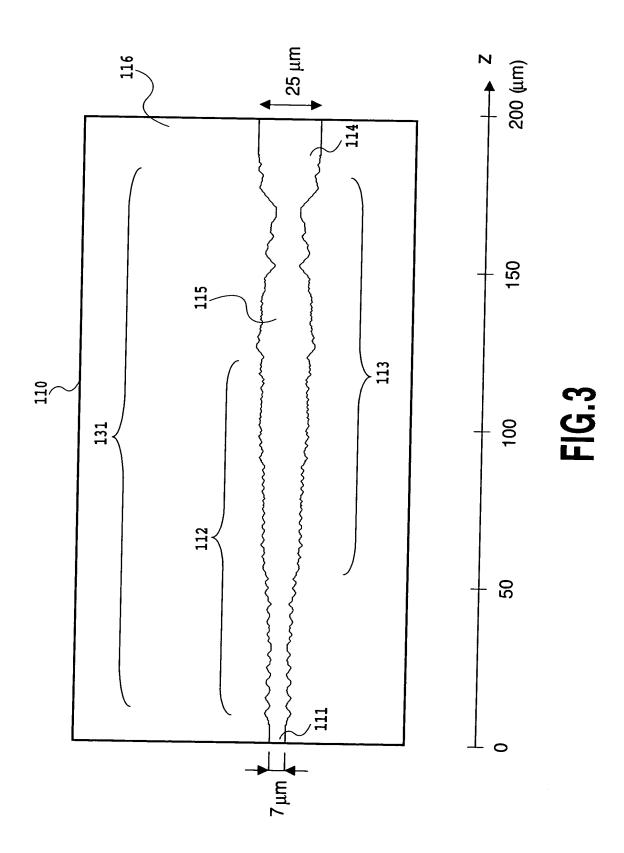
Inventor: Takashi Saida et al. Docket No. 14321.89



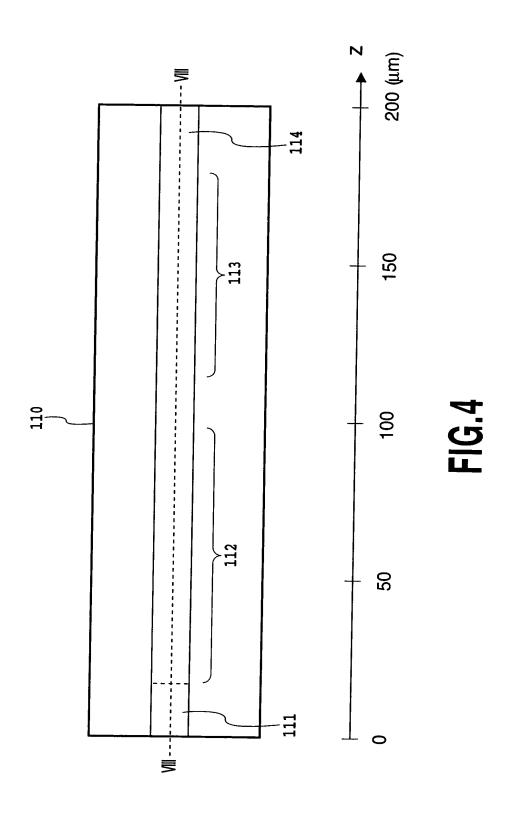
Inventor: Takashi Saida et al. Docket No. 14321.89 2/44



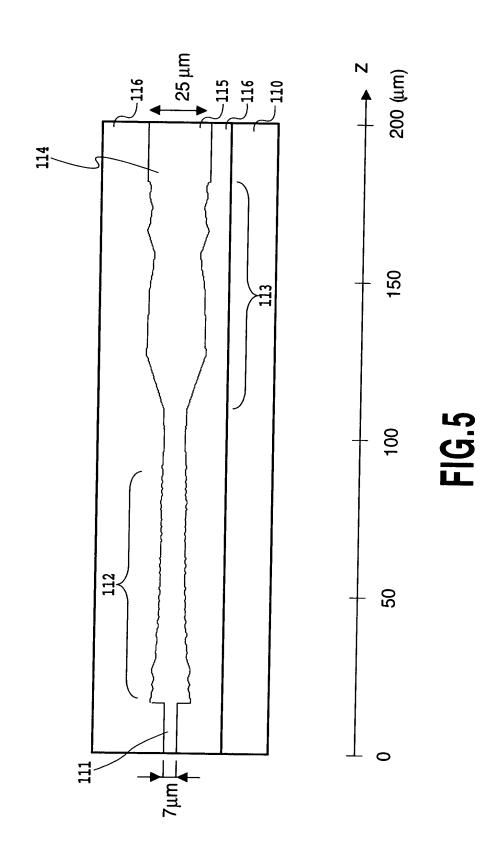
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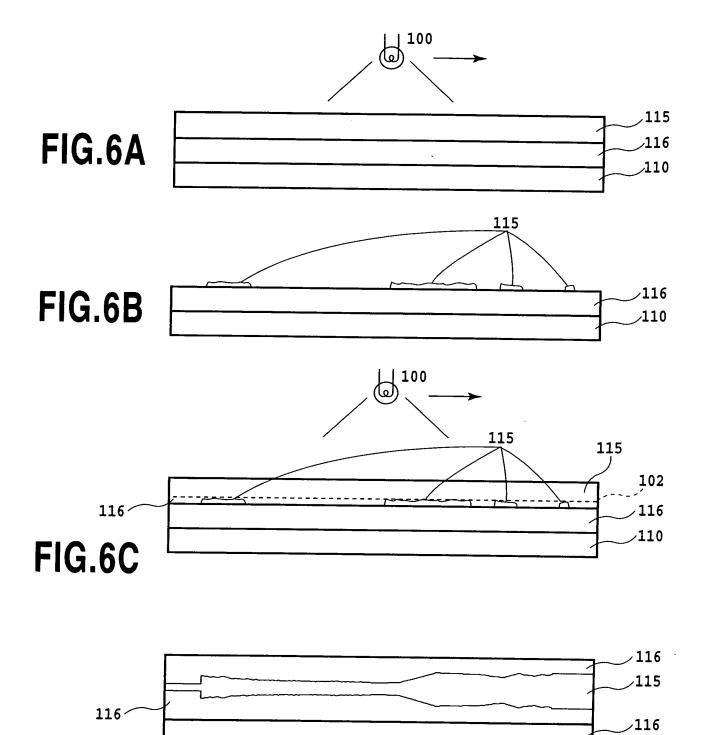
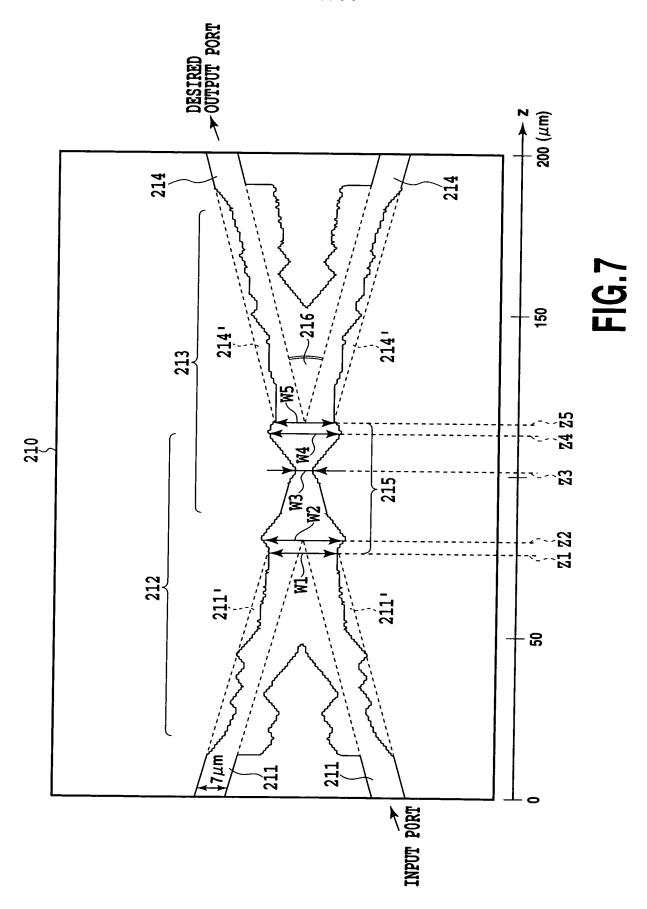


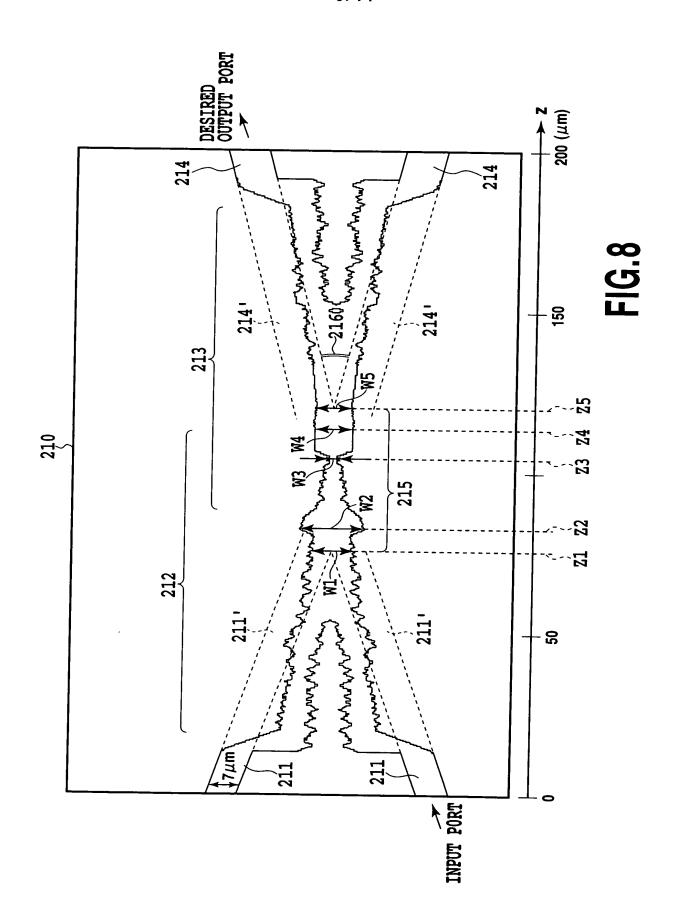
FIG.6D

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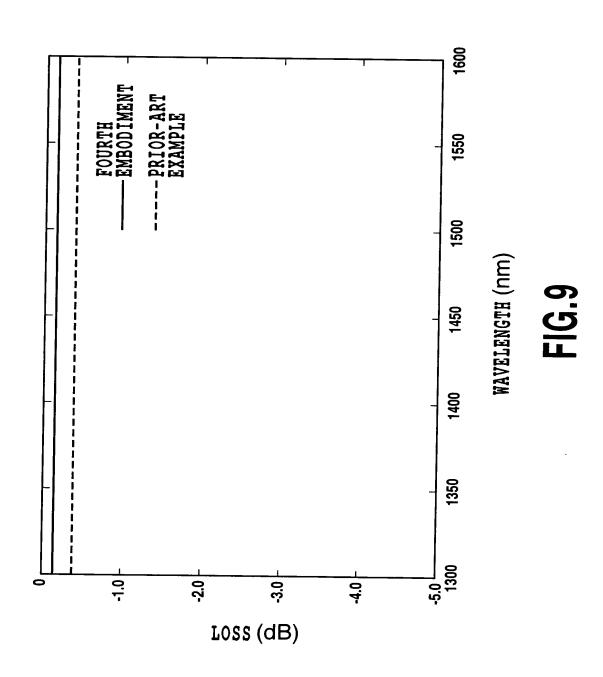
Inventor: Takashi Saida et al. Docket No. 14321.89



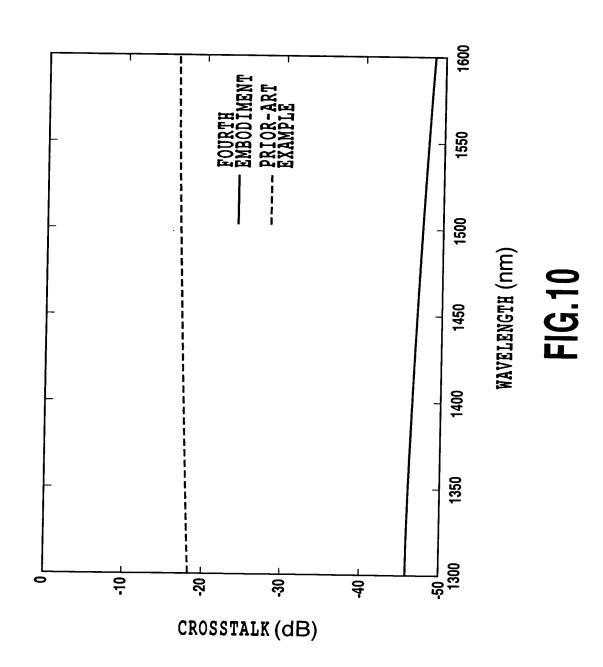
Inventor: Takashi Saida et al. Docket No. 14321.89



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Inventor: Takashi Saida et al. Docket No. 14321.89

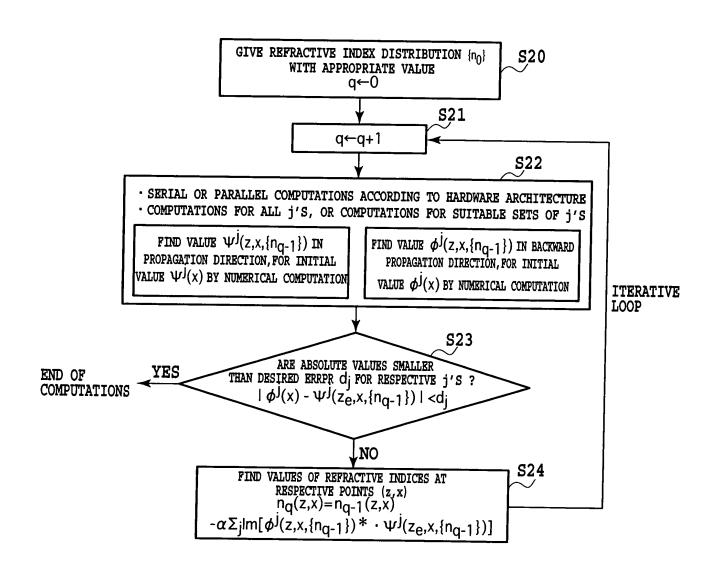
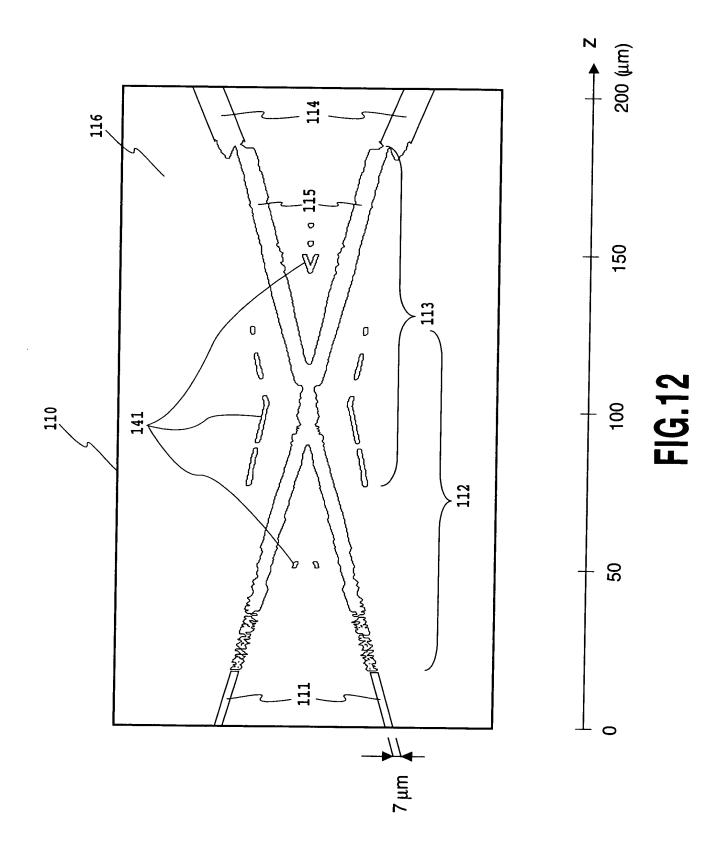


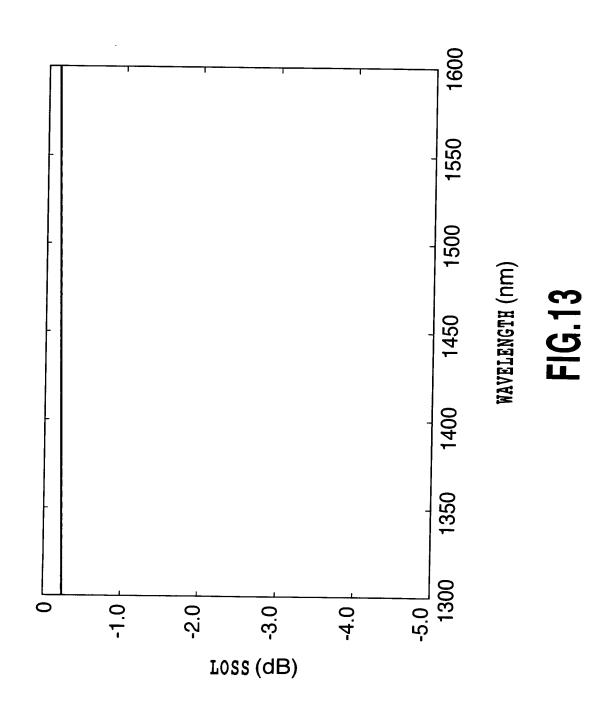
FIG.11

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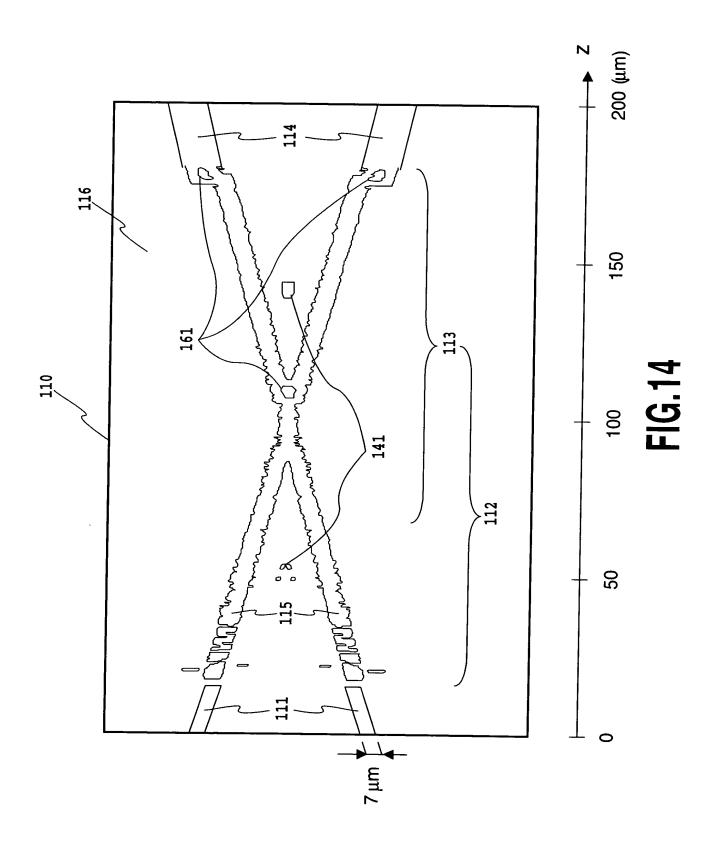
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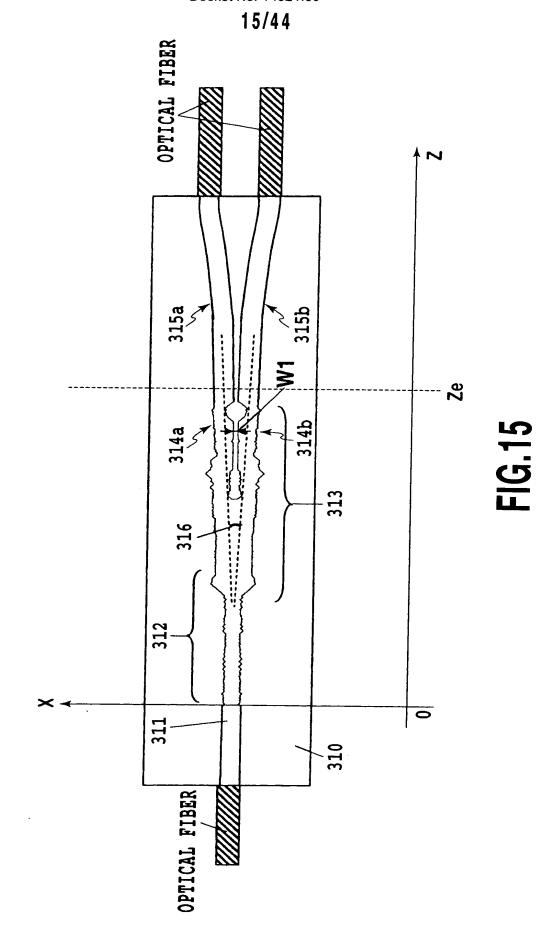
FLAT OPTICAL CIRCUIT, DESIGN METHOD FOR WAVE MOTION PROPAGATING CIRCUIT AND COMPUTER PROGRAM Inventor: Takashi Saida et al.

Docket No. 14321.89

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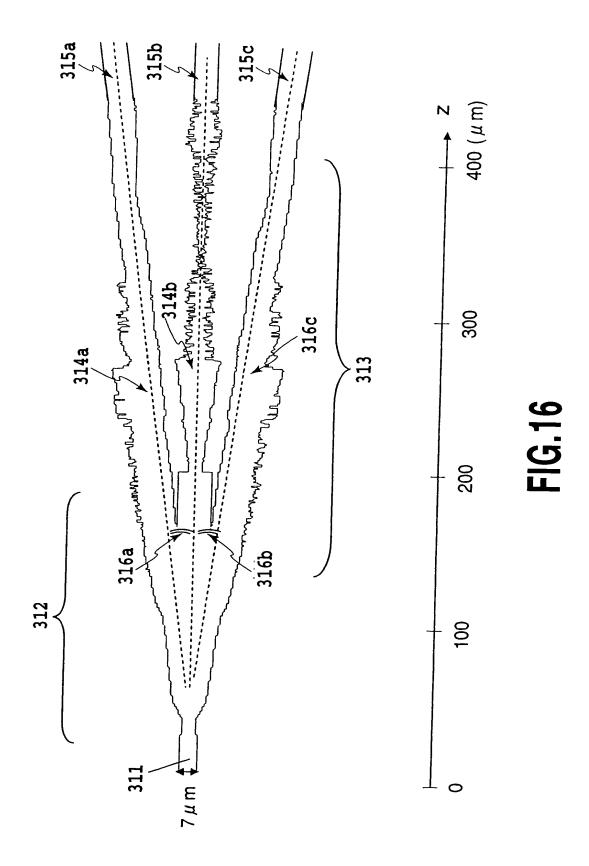


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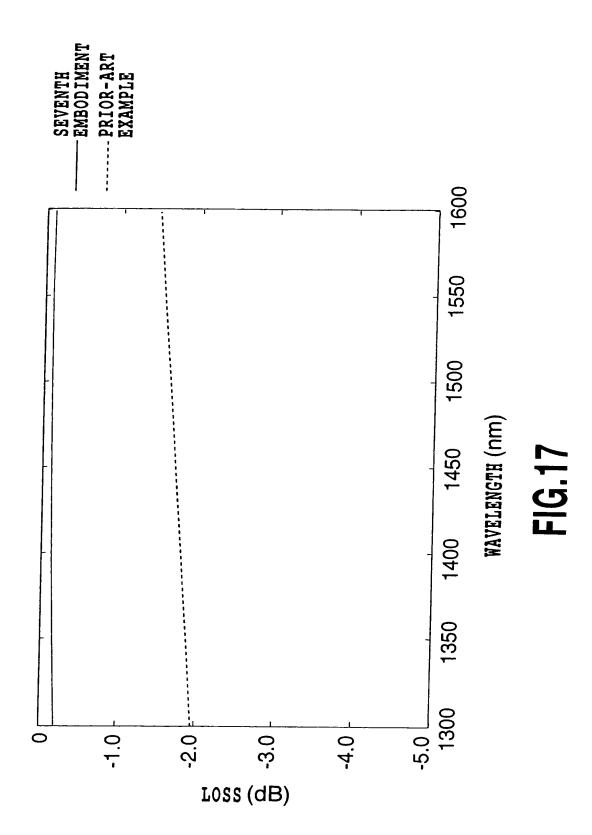


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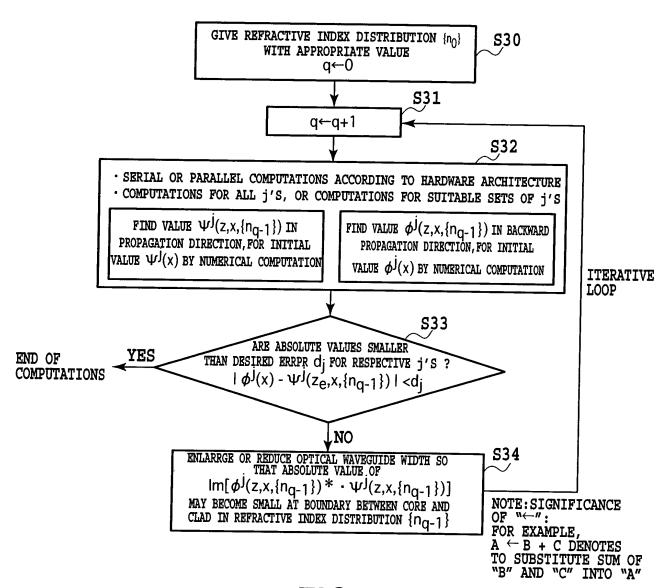
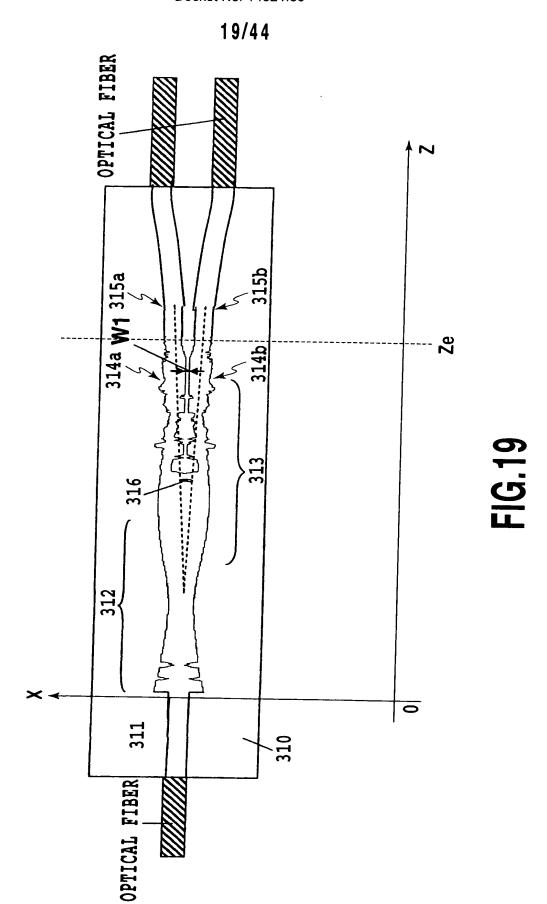
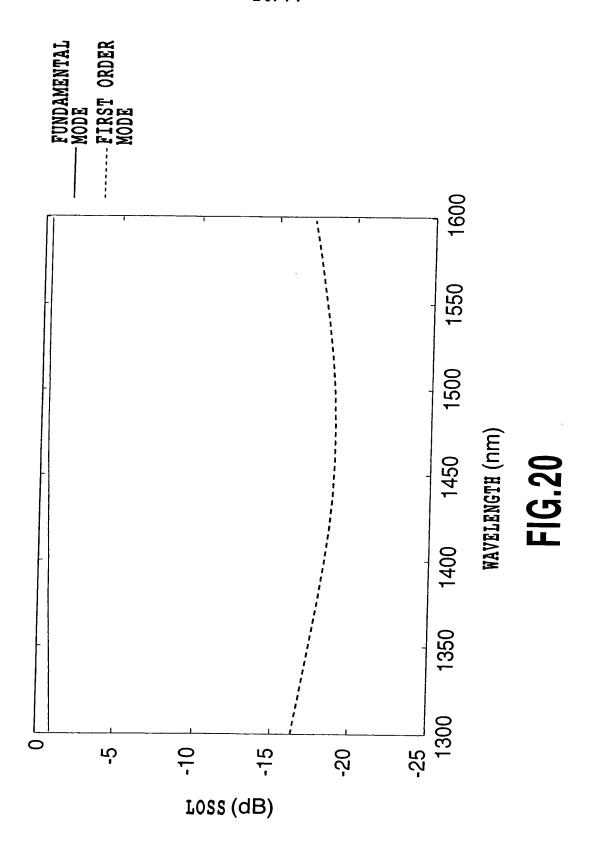


FIG.18

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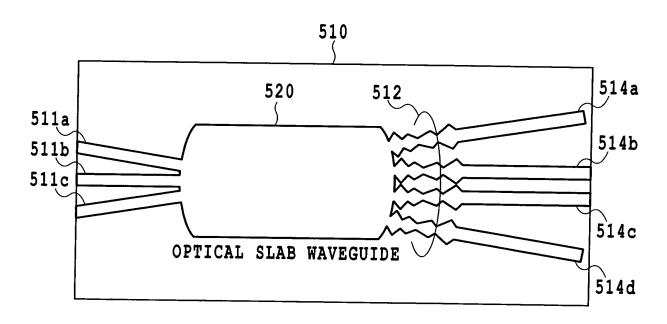


FIG.21

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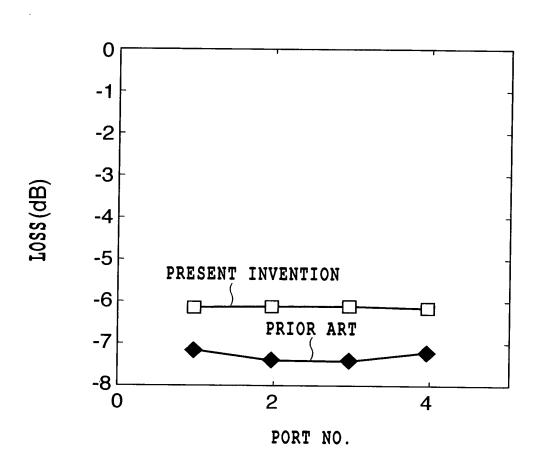
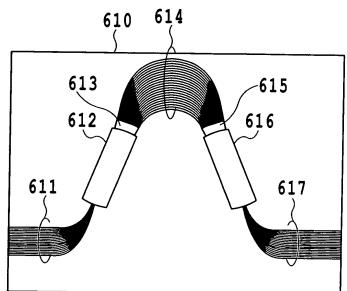


FIG.22

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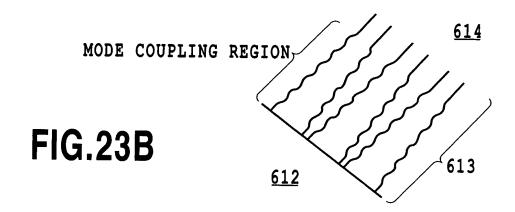
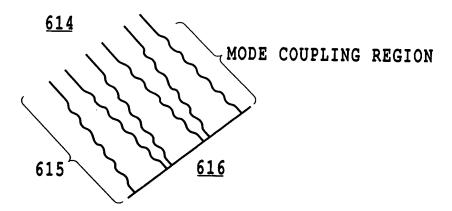
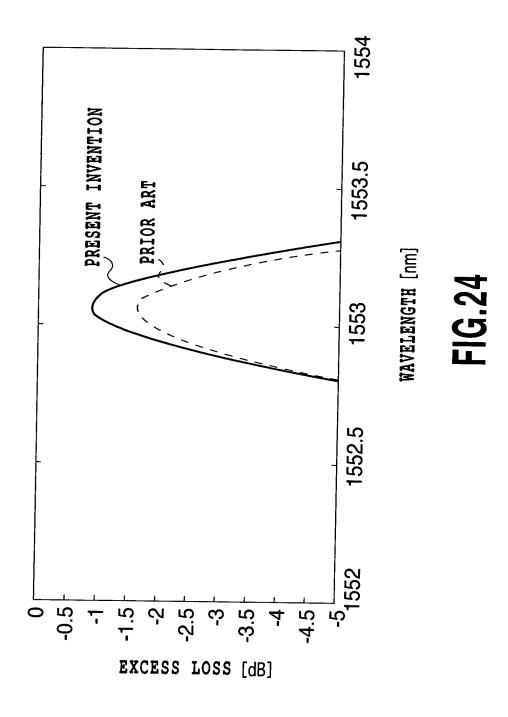


FIG.23C



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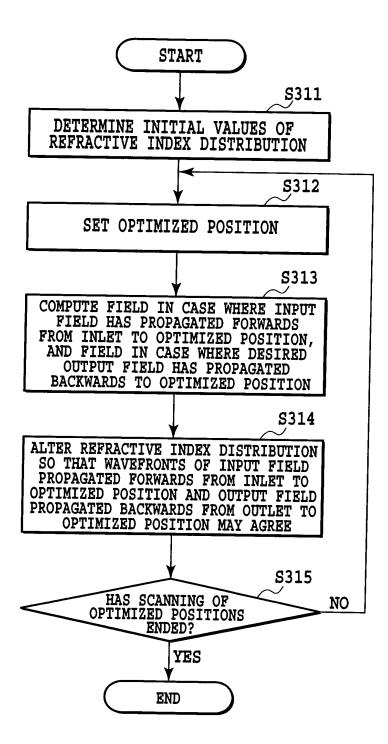
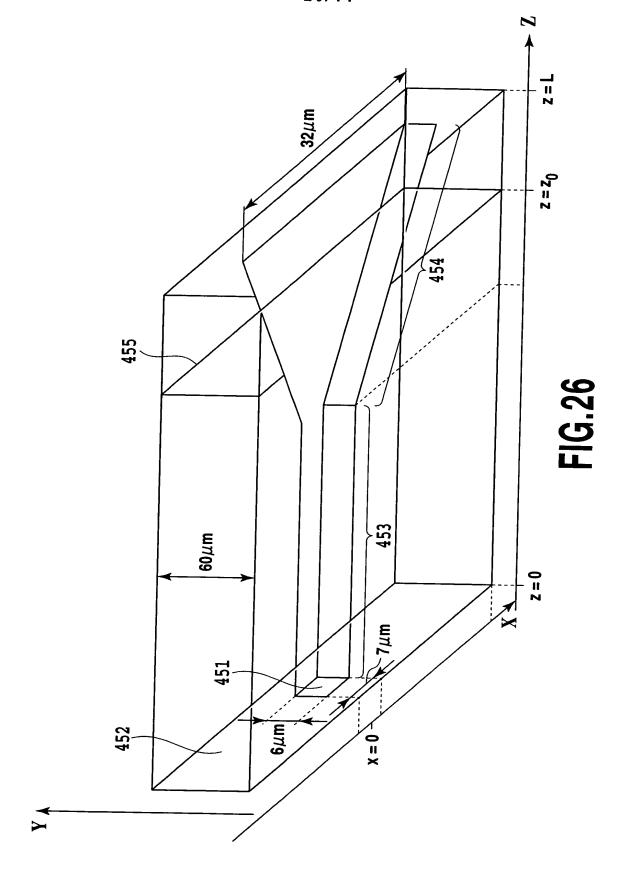


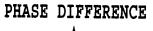
FIG.25

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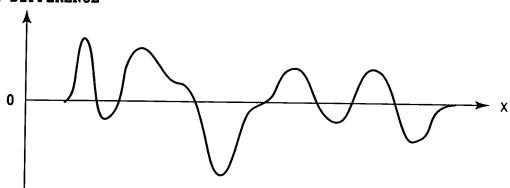


FIG.27A

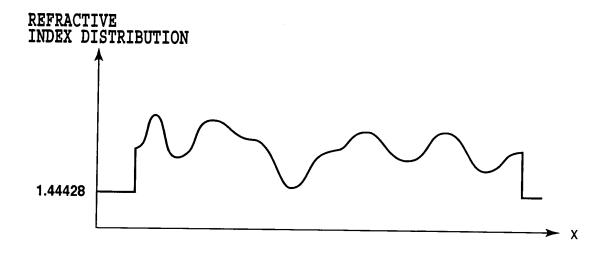


FIG.27B

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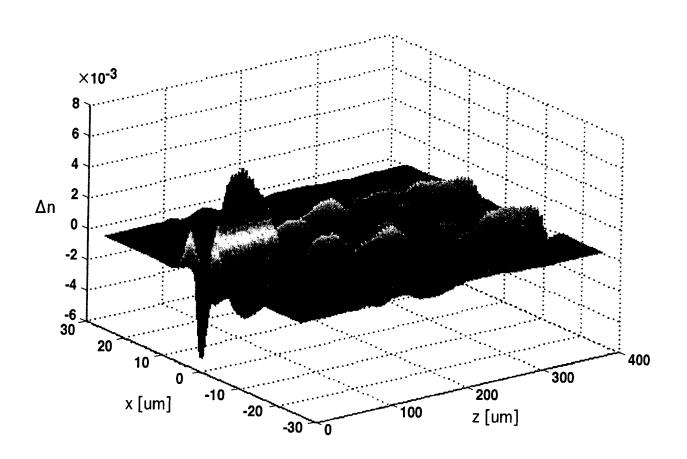
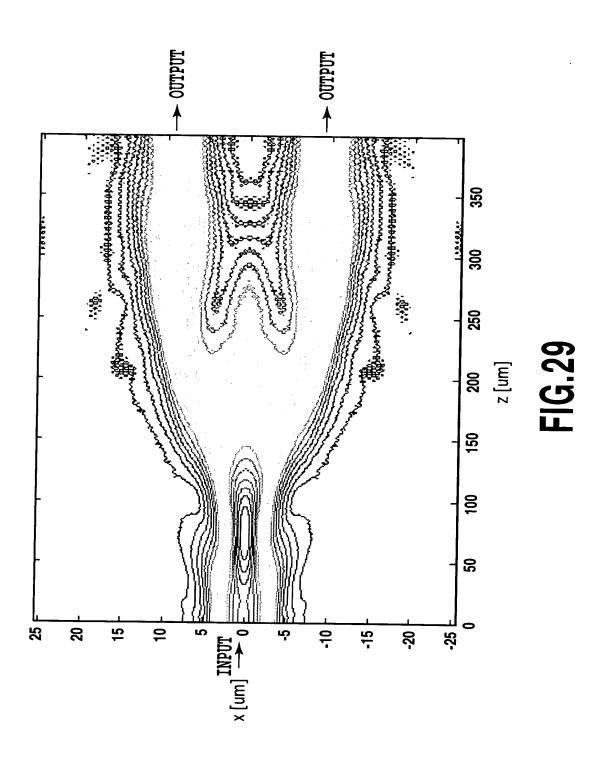


FIG.28

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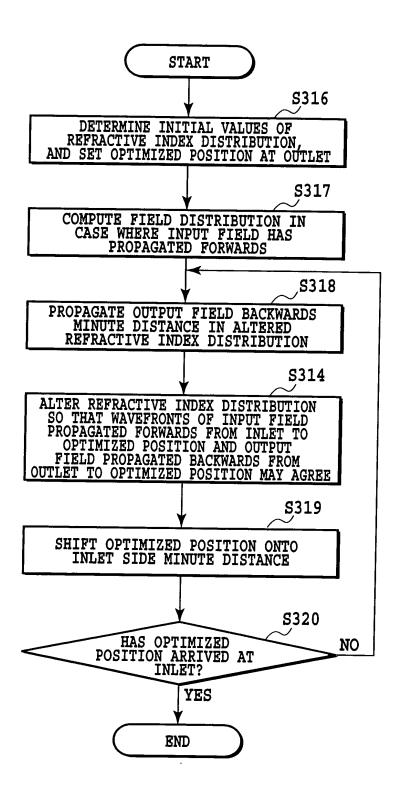
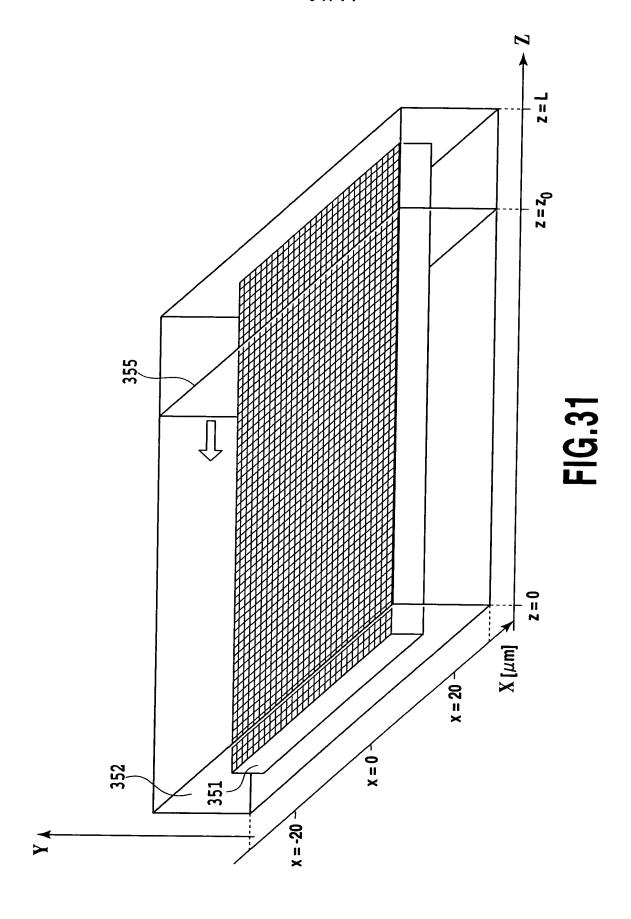


FIG.30

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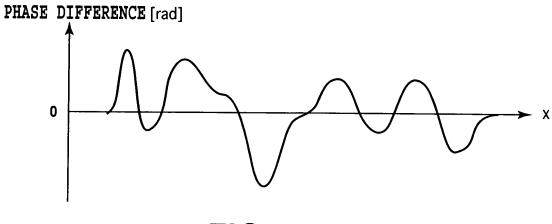
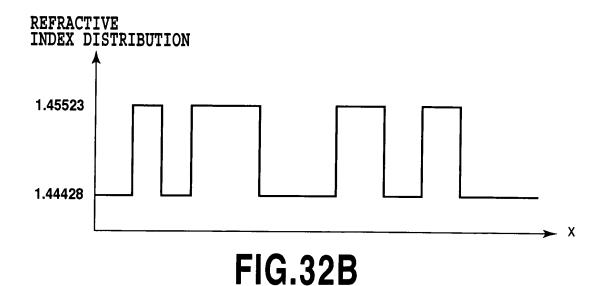
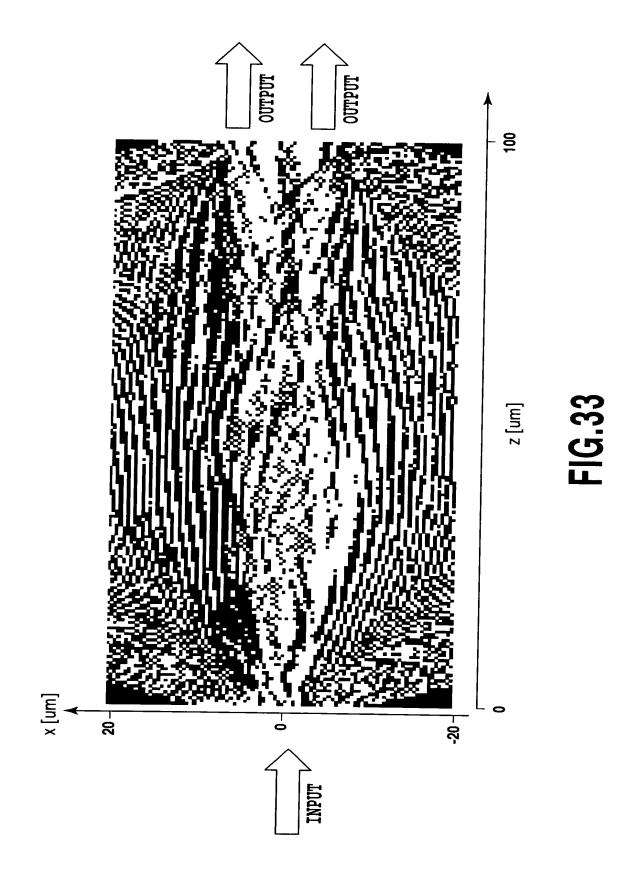


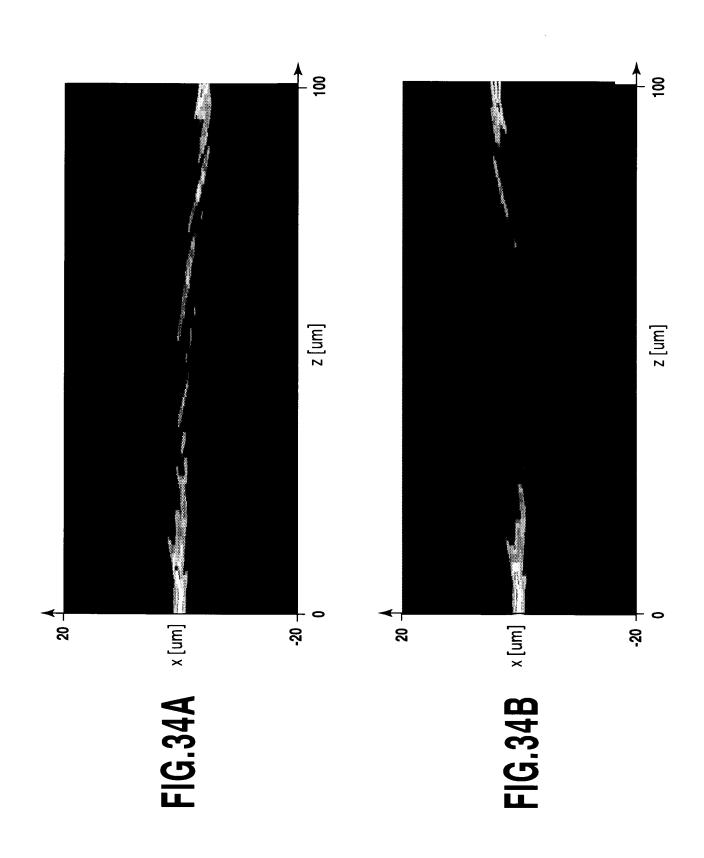
FIG.32A



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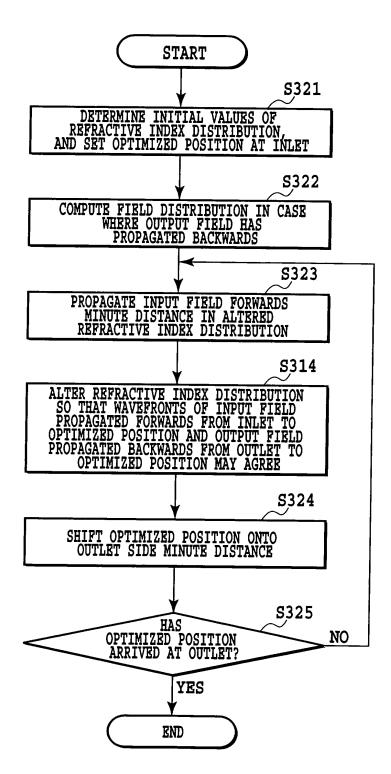
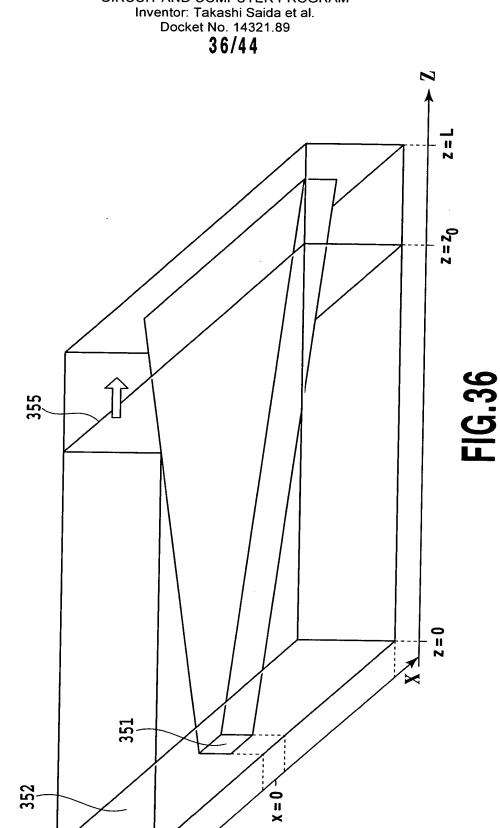
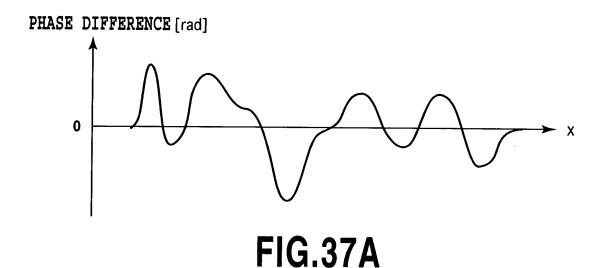
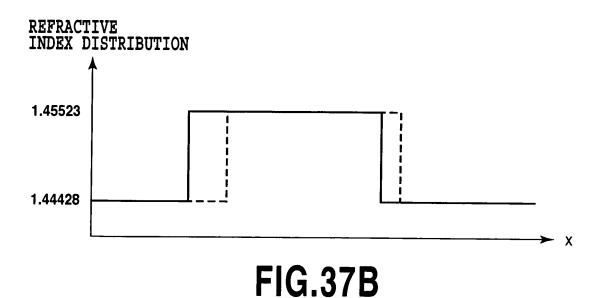


FIG.35

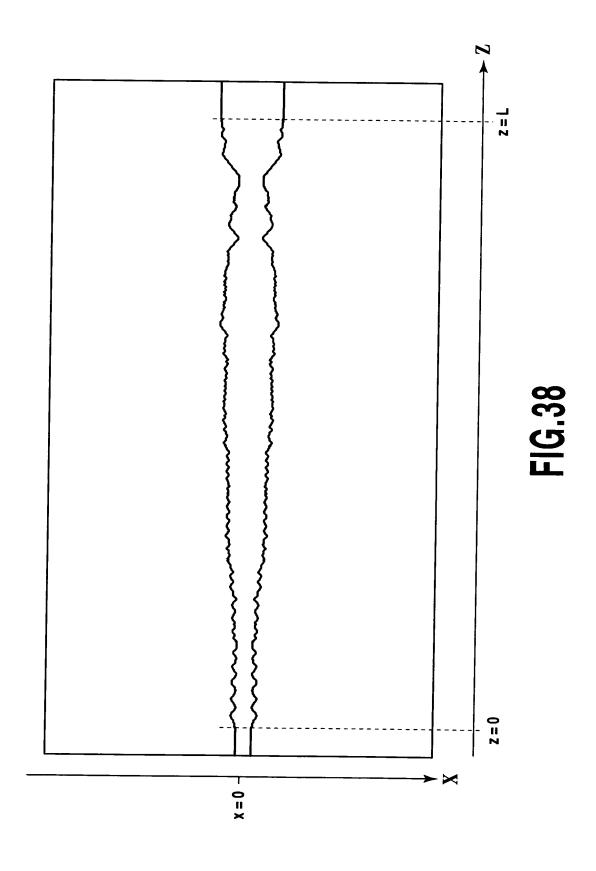


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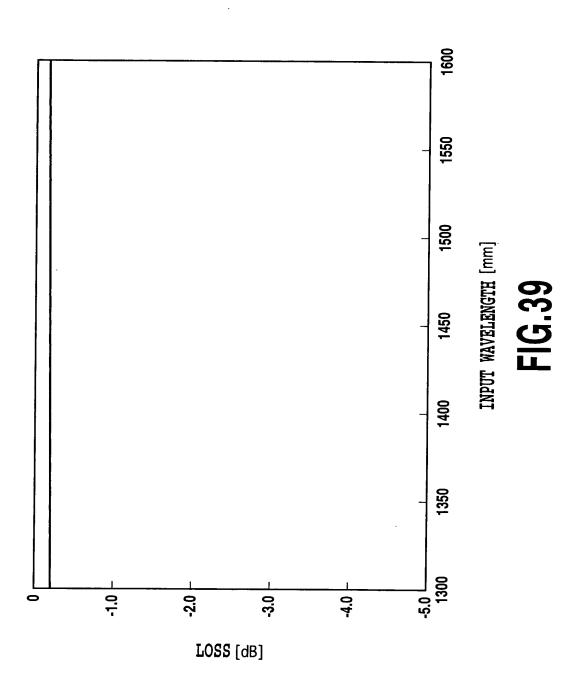




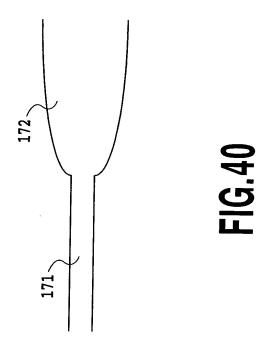
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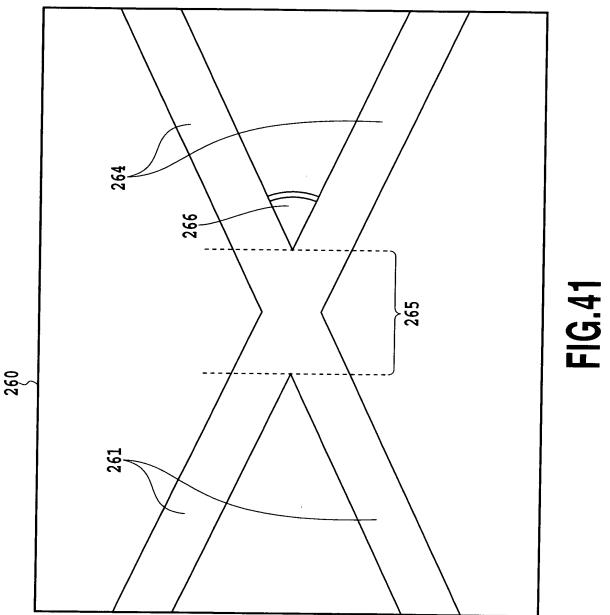
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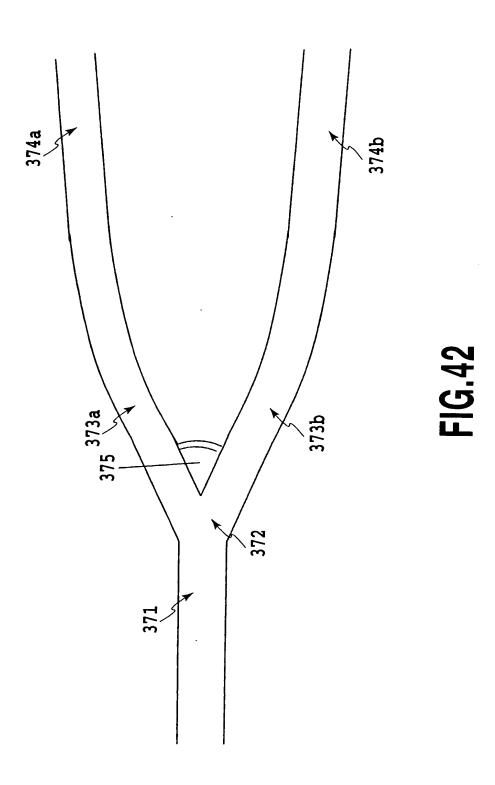


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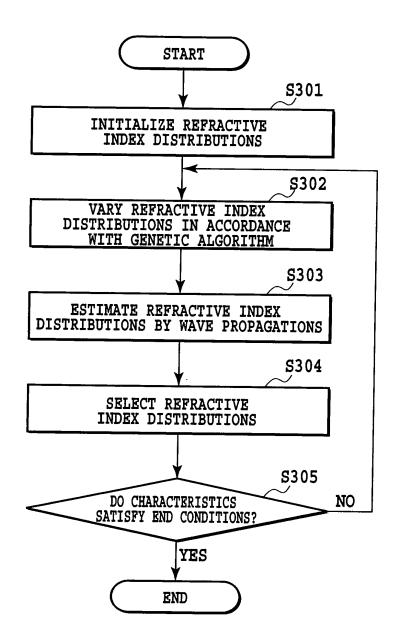


FIG.43

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